

Amendments to the Claims:

This listing of the claims will replace all prior versions and listings of claims in this application.

Listing of Claims:

1. – 38. (canceled).

39. (currently amended) A method for updating system software in a wireless communications device, the method comprising:

copying a patch manager code section stored in a nonvolatile memory to a volatile memory;

providing a patch manager to manage system software updates;

receiving a broadcasted system software update comprising an update code section and an update patch manager;

storing the system software update on a file system section of the [[a]] nonvolatile memory module comprising a file system section, a patch manager run-time instruction (PMRTI) section, a code storage section, and a patch manager code section;

updating at least a portion of a code section of a plurality of code sections stored in a code storage section of the nonvolatile memory with the update code section, each code section of the plurality of code sections forming the system software into a plurality of symbol libraries, comprising a first at least one symbol library, a second symbol library, and a third symbol library, wherein each symbol library comprises having a plurality of symbols having a of related functionality;

arranging the first symbol library, the second symbol library, and the third symbol library in at least two code sections, wherein the second symbol library and the third symbol library are arranged contiguously within a single code section on the non-volatile memory module;

updating at least a portion of a patch manager code section of the nonvolatile memory with at least a portion of the update patch manager, comprising:

- updating a code section address table of the patch manager code section which stores a code section identifier and a start address location reference for each code section of the plurality of ~~of the at least two~~ code sections;
- updating a symbol offset address table which stores an offset reference for each symbol of the plurality of symbols in the at least one symbol library of the each code section, the offset reference comprising an offset value derived from the start address of the each code section, and a code section address table reference for each of the first, second and third symbol libraries.

40. (currently amended) The method of claim 39 further comprising executing the system software update from the nonvolatile memory, loading the system software update from the patch manager code section and the code storage section within the nonvolatile memory module to a volatile memory component and performing at least one requested action.

41. (currently amended) The method of claim ~~39~~ 40, wherein each symbol of the plurality of symbols is associated with a symbol access code, further comprising: ~~forming the system software into the plurality of symbol libraries, further comprises forming a symbol access code and~~ arranging the symbol access code in the corresponding symbol library.

42. (currently amended) The method of claim 41, further comprising referencing the symbol access code to calculate an address of a sought symbol comprising:
accessing the symbol offset address table to determine a corresponding code section identifier ~~address table reference~~ and a corresponding offset value; ~~reference and~~

accessing the code section address table to determine a start address of the corresponding code section identifier; and location reference associated with the corresponding code section address table reference

calculating the address of the sought symbol by adding the offset value to the start address.

43. (currently amended) The method of claim 41 further comprising associating a first symbol access code with the a first symbol library of the at least one symbol library, associating a second symbol access code with the a second symbol library of the at least one symbol library, associating a third symbol access code with the a third symbol library of the at least one symbol library.

44. (currently amended) The method of claim 43, wherein the third symbol access code is stored contiguously to the second symbol library on the memory component.

45. (currently amended) A wireless communication device comprising:

a nonvolatile memory module comprising [[,]]: a patch management-run time instructions (PMRTI)-section,

a file system section (FSS) [[,]] for storing a received updated code section and a received updated patch manager code section;

a code storage section [[,]] for storing at least one code section, each code section of the at least one code section comprising at least one symbol library comprising a plurality of symbols having a related functionality; and

a patch manager code section [[;]] comprising:

a memory component comprising a first symbol library, a second symbol library, and a third symbol library, wherein each symbol library comprises a plurality of symbols having a related functionality, the memory component having arranged the first symbol library, the second symbol library, and the third symbol

library in at least two code sections, in which the second symbol library and the third symbol library are arranged contiguously within a single code section;
a code section address table which stores a code section location reference identifier and a start address for the each of the at least two code section sections; and
a symbol offset address table which stores an offset reference for each symbol of the plurality of symbols in the at least one symbol library stored in the each code section, the offset reference comprising an offset value derived from the start address of the each code section, and a code section address table reference for each of the first, second and third symbol libraries.

46. (currently amended) The wireless communication device of claim 45, wherein the updated patch manager code management run-time instructions (PMRTI) section is configured to enable the a system software update updates to be implemented on the wireless communication device.

47. (currently amended) The wireless communication device of claim 45, wherein the patch manager code section further comprises:
a read-write data section;
a symbol accessor code section;
a symbol accessor code address section; and
a patch library. memory component is located on the nonvolatile memory.

48. (currently amended) The wireless communication device of claim 45, wherein the patch manager code section is loaded into memory component is located on a volatile memory upon a reset condition.

49. (currently amended) The wireless communication device of claim 45, wherein at least a portion of the patch manager code section is overwritten with the updated patch manager code section. ~~the memory component is located on the nonvolatile memory and a volatile memory.~~

50. (currently amended) The wireless communication device of claim 45 wherein the at least one code section of the code storage section comprises ~~the~~ at least two code sections.

51. (currently amended) The wireless communication device of claim 50 wherein the received updated code section and the received updated patch manager code section define a system software update, and wherein each code section of the at least two code sections stores at least part of the ~~or a whole~~ system software update.

52. (currently amended) The wireless communication device of claim 51 wherein the patch manager code section is configured to control the system software update ~~updates that are downloaded.~~

53. (new) The wireless communication device of claim 45, wherein the at least one symbol library comprises a first symbol library, a second symbol library, and a third symbol library wherein the second symbol library and the third symbol library are arranged contiguously within a single code section.